

## **REMARKS**

Claims 1-7 and 12 are now pending in the application. Claims 8-11 and 13-15 are withdrawn without disclaimer or prejudice to the subject matter contained therein. The Examiner is respectfully requested to reconsider and withdraw the rejection in view of the amendments and remarks contained herein.

## **DRAWINGS**

The drawings stand objected to for certain informalities. Applicant attaches revised drawings for the Examiner's approval. In the "Replacement Sheets," Applicant amends the Figures to include a prior art designation as the Examiner suggests.

## **REJECTION UNDER 35 U.S.C. § 102**

Claims 1-3 stand rejected under 35 U.S.C. § 102(b) as being anticipated by Ash (U.S. Pat. Pub. No. 20020125694). This rejection is respectfully traversed.

Applicant's claim 1 is directed to a clock converter that synchronizes the phases of a phase locked loop (PLL) feedback signal. The clock converter includes a voltage controlled oscillating means for outputting signals that are used in a PLL. In particular, claim 1 recites a "buffer means forming a portion of the positive feedback loop." A positive feedback signal is output "from one output terminal of" the buffer means. The PLL feedback signal is output "from another output terminal" of the buffer means. In other words, both the positive feedback signal and the PLL feedback signal are output from a buffer means that is included in "a portion of the positive feedback loop. In particular, the PLL feedback signal is output from a portion of the positive feedback loop

whereas conventional oscillating circuits use the output of the of the oscillating circuit as the PLL feedback signal (as shown by Applicant's FIG. 10).

Applicant respectfully submits that Ash does not disclose such a structure. Ash discloses a SAW resonator circuit 102 and a buffer means 110 as shown in Figure 1. The buffer means 110 outputs signals from an amplifier 113 to the SAW resonator circuit 102 and outputs signals 102a and 102b from an amplifier 112. The SAW resonator circuit 102 and the buffer means 110 form an oscillator 100. The output signals 102a and 102b are the outputs of the oscillator 100. The Examiner suggests that the output signals for the amplifier 113 are positive feedback signals and that the signals 102a and 102b are PLL feedback signals. Applicant respectfully notes that such a construction requires the signals 102a and 102b to function as both output signals and PLL feedback signals of a PLL. In other words, at best, the PLL feedback signals are output from the output portion of the oscillator. In contrast, claim 1 requires that the PFF feedback signal is output from a buffer means forming a portion of the positive feedback loop. Ash does not show, teach, or suggest a PLL feedback signal that is output from a buffer means forming a portion of the positive feedback loop. Applicant respectfully submits that claim 1, as well as its corresponding dependent claims, should be in condition for allowance.

#### **ALLOWABLE SUBJECT MATTER**

The Examiner states that claims 4-7 and 12 would be allowable if rewritten in independent form. Applicant thanks the Examiner for the allowable subject matter.

However, Applicant elects to defer amending the claims into independent form until after the above remarks are considered.

**CONCLUSION**

It is believed that all of the stated grounds of rejection have been properly traversed, accommodated, or rendered moot. Applicant therefore respectfully requests that the Examiner reconsider and withdraw all presently outstanding rejections. It is believed that a full and complete response has been made to the outstanding Office Action, and as such, the present application is in condition for allowance. Thus, prompt and favorable consideration of this amendment is respectfully requested. If the Examiner believes that personal communication will expedite prosecution of this application, the Examiner is invited to telephone the undersigned at (248) 641-1600.

Respectfully submitted,

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**AMENDMENTS TO THE DRAWINGS**

The attached "Replacement Sheets," which include Figures 10-12, replace the original sheets including Figures 10-12.

Attachment: Replacement Sheets